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(54) Video signal error detection systems

(57) Mobile phones now transmit video signals to each other. Such video signals are transmitted at a relatively slow bit rates. The signals transmitted are compressed using inter-frame prediction where sufficient similarity exists between successive picture frames. The receiver includes a decoder (18) which acts to re-constitute the compressed signals received using signals stored on a buffer (22) representing a previous picture frame. A mode decision circuit (24) compares the signal at the output of the decoder (18) with the previous

signal stored in the buffer (22) and using the same decision criterion as used for the original compression generates a signal which indicates that either the signal is suitable for inter-frame prediction or not. This signal is then compared in a comparator (26) with the signal in the decoder (18) indicating whether or not the current signal was inter-frame predicted or not. If equality is not detected, then an error may have taken place. Then, error concealment techniques can be used to reduce the effects of the errors.

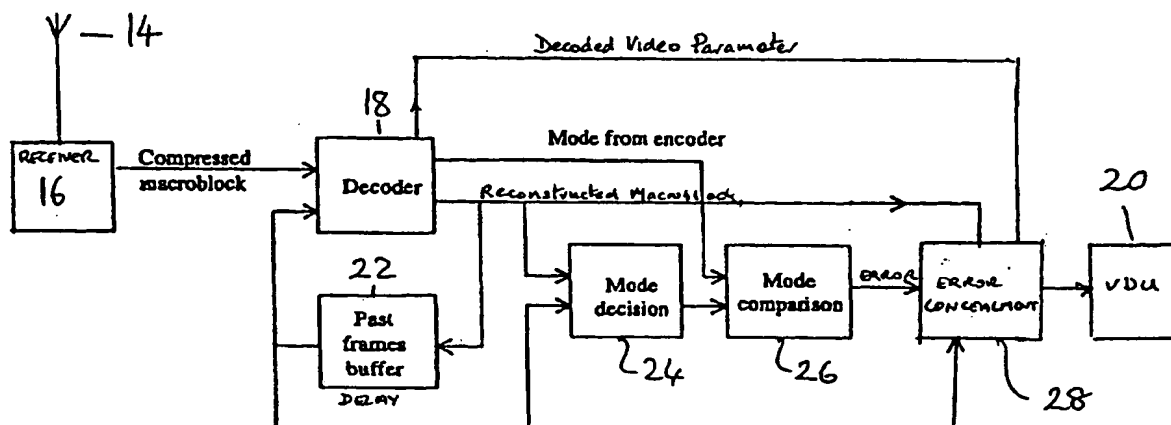


FIG 2

Description

[0001] The present invention relates to video signal error detection systems.

[0002] Mobile phones now have the facility to transmit and receive video signals so that the communicating parties can view each other while speaking.

[0003] Because of restricted bandwidths, the signals are transmitted at low bit rates. Because mobile phone networks are now so extensive and the transmission medium is air, the transmitted signals are more susceptible to corruption, for example, due to channel errors. Accordingly, there is a need to check received video signals for errors.

[0004] The video signals are usually transmitted in a succession of successive macroblocks. Each macroblock may comprise a block of 16 x 16 pixels of a picture frame. A simple error check may be carried out in the mobile phone receiving the signal, by counting the coefficients in the macroblock. In a 16 x 16 macroblock there will be a maximum of 256 coefficients and, if more are counted, it can be assumed that an error has occurred. In this event, the macroblock being received may be suppressed and replaced by the corresponding block in the previous frame or other error concealment techniques may be used.

[0005] However, other errors may not be so easy to detect.

[0006] It is an object of the invention to provide an improved video signal error detection system.

[0007] According to the present invention there is provided a video signal error detection system for use in a receiver having a decoder for receiving encoded video signals produced by an encoder in a transmitter, the encoder being selectively operative to transmit only differences between selected macroblocks in successive frames when a specific difference criteria between said successive blocks is not exceeded, the detection system comprising a first comparator for comparing macroblocks in successive frames in the output signal from said decoder, applying said specific difference criteria to provide an indication of whether inter-frame prediction should apply or not, and a second comparator for comparing the output from the first comparator with an output from the decoder indicative of whether or not the last macroblock received was in inter-frame prediction format or not and operable to generate an error signal when a divergence is detected.

[0008] The video signal error detection system embodying the present invention, will now be described, by way of example, with reference to the accompanying diagrammatic drawings, in which:

Figure 1 is a block diagram of a video transmission system; and

Figure 2 is a block diagram of a video reception system.

[0009] The video transmission system shown in Figure 1 comprises a video camera 2 which generates a video output signal representative of a picture frame.

[0010] The output of the camera 2 is fed to an encoder 8. The output of the encoder 8 is fed via a delay circuit 4 to a buffer memory 6 and directly to a transmitter 10 which transmits the signal via an antenna 12 to a remote station. The encoder 8 compares the current picture frame signals being received from the camera 2 with the immediately preceding picture frame signals as stored in the buffer memory 6.

[0011] The encoder 8 exploits the correlation between successive frames. The current video coding standards use block matching motion compensation technique for inter-frame prediction. In this approach, if the current picture block is similar to another block in the previous frame (picture) within the search area, the displacement (motion) vector and the difference between those two blocks are encoded (INTER mode). Otherwise, the current picture is encoded without any inter-frame prediction (INTRA mode).

[0012] Each block, hereinafter called a macroblock, may comprise a 16 x 16 pixel block.

[0013] The video reception system, shown in Figure 2, includes an antenna 14 which feeds a receiver 16. The output of the receiver 16 is fed to a decoder 18. The decoder 18 decodes the bit stream which also includes a mode indicator signal from the receiver. This mode information is fed to a mode decision circuit 24. The reconstructed picture is fed to an error concealment circuit 28, and also to a buffer 22 so that the buffer 22 is always updated to provide the previous (the immediately preceding) picture frame.

[0014] The output of the error concealment circuit 28 is displayed on a visual display unit (VDU) 20.

[0015] The mode decision circuit 24 responds to the output of the decoder 18 and the buffer 22 to carry out a similar decision making process to that carried out in the encoder 8 ie it tries to predict whether the two macroblocks being compared qualify for INTER or INTRA mode treatment. A comparison circuit 26 then compares the output of the decision circuit with the mode indicator from the decoder 18. If concordance is not detected, there is a strong possibility of error. The error concealment circuit 28 tries to recover corrupted data if an error indication is signalled from the comparison circuit 26, using established techniques.

[0016] The mode decision circuit 24 may be constructed in a manner similar to the video codec test model Version 8 (TMN8) described by Thomas R Gardos in the proceedings of the video coding experts group question 15 (Q15-A-59) Portland 24-27 June 1997, which material is incorporated herein by reference.

[0017] In TMN8, the following parameters are calculated to make the INTRA/INTER decision:

$$MB_mean = (\sum_{i=1, j=1}^{16,16} original(i, j)) / 256$$

$$A = \sum_{i=1, j=1}^{16,16} |original(i, j) - MB_mean|$$

INTRA mode is chosen if $A < B$
Where,

$$B = \sum_{i=1, j=1}^{16,16} |original(i, j) - previous(i, j)| - \beta$$

$\beta = \{600 \text{ mv} = 0$
 $\{500 \text{ mv} \neq 0$

[0018] The parameter *previous (i,j)* corresponds to the best match block in the previous frame used for motion compensation, and *mv* is the motion vector.

[0019] Since the decoder does not have the original values, the term "original" has to be replaced by the term "re-constructed". The change from two-outcome to three-outcome case is achieved by considering an error margin *E* due to the problems discussed above:

INTRA mode if $A < B - E$
UNKNOWN if $B - E < A < B + E$
INTER mode if $B + E < A$

[0020] A mode change from INTER to INTRA (or vice versa) indicates that the current block is corrupted, and therefore appropriate error concealment techniques should be used, such as the one hereinbefore described.

[0021] Obviously, the value of *E* should be tuned depending on the system in use. It is a trade-off between the accuracy and rate of detection of the corruption blocks. If the decoder and encoder both employ the same method for mode selection, then a smaller *E* can be selected.

[0022] The difference between the original and decoded pixel values depending on the quantization parameter *q*. Therefore, *E* should be a function of *q*. This function should be determined by considering the quantizer. For the TMN8 quantizer, *E* value is defined as follows:

$E = \{256 \text{ if } q < 4$
 $\{64q \text{ if } q \geq 4$

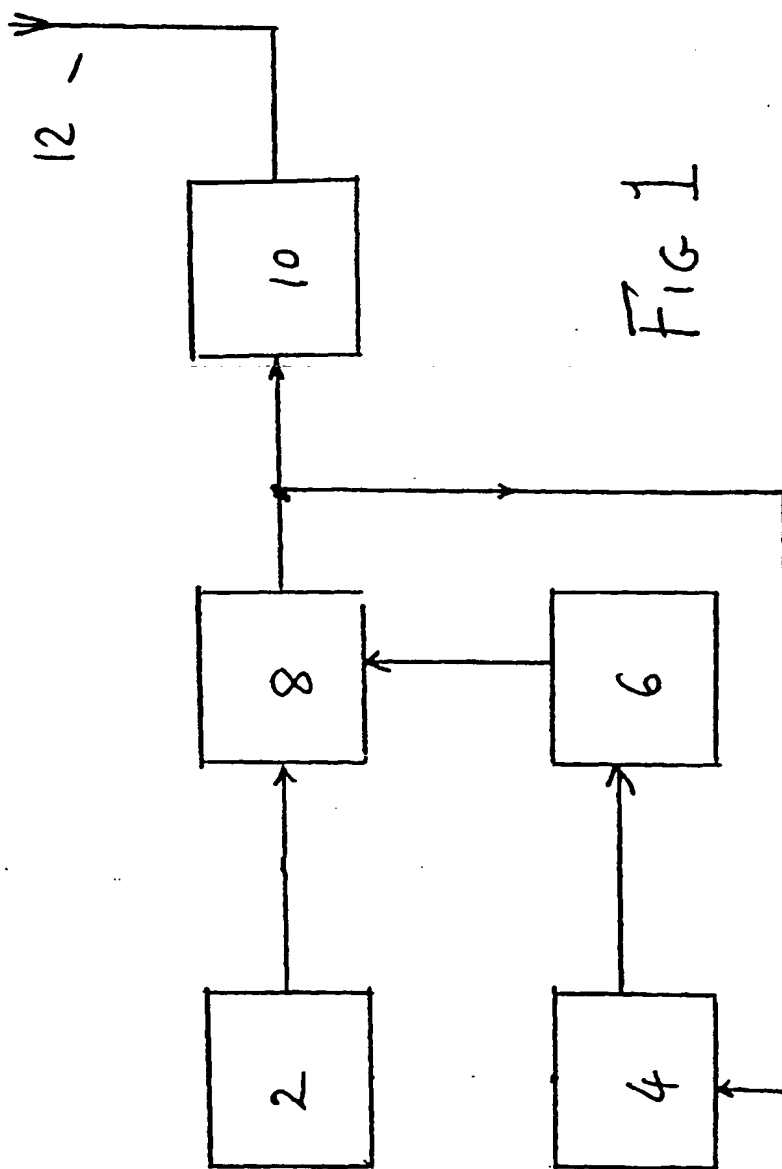
Claims

1. A video signal error detection system for use in a receiver having a decoder for receiving encoded video signals produced by an encoder in a transmitter, the encoder being selectively operative to transmit only differences between selected macroblocks in successive frames when a specific difference criteria between said successive blocks is not exceeded, the detection system comprising a first comparator for comparing macroblocks in successive frames in the output signal from said decoder, applying said specific difference criteria to provide an indication

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of whether inter-frame prediction should apply or not, and a second comparator for comparing the output from the first comparator with an output from the decoder indicative of whether or not the last macroblock received was in inter-frame prediction format or not and operable to generate an error signal when a divergence is detected.

- 5 2. A system according to Claim 1, including an error concealment circuit responsive to the output of the decoder and buffer to recover corrupted data when the second comparator indicates an error.
3. A system according to Claim 1 or to Claim 2, wherein the first comparator acts as a mode decision circuit capable of generating one of three outcomes namely, inter-frame prediction, no inter-frame prediction and unknown.
- 10 4. A system according to Claim 3, wherein the mode decision circuit responds to each macroblock of each frame from the output from the decoder to determine a mean value for the pixels and responds to the difference A between the reconstructed values of the pixels in said macroblock and the said mean value, the difference B between the reconstructed values of the pixels of the present macroblock and the reconstructed values of the pixels of the
- 15 corresponding macroblock of the immediately preceding frame and the error margin E to provide a first outcome if $A < B - E$ a second outcome if $B - E < A < B + E$ and a third outcome if $B + E < A$.
5. A system according to Claim 2, wherein the error concealment circuit acts to replace each corrupted macroblock with a corresponding macroblock in the immediately preceding frame.



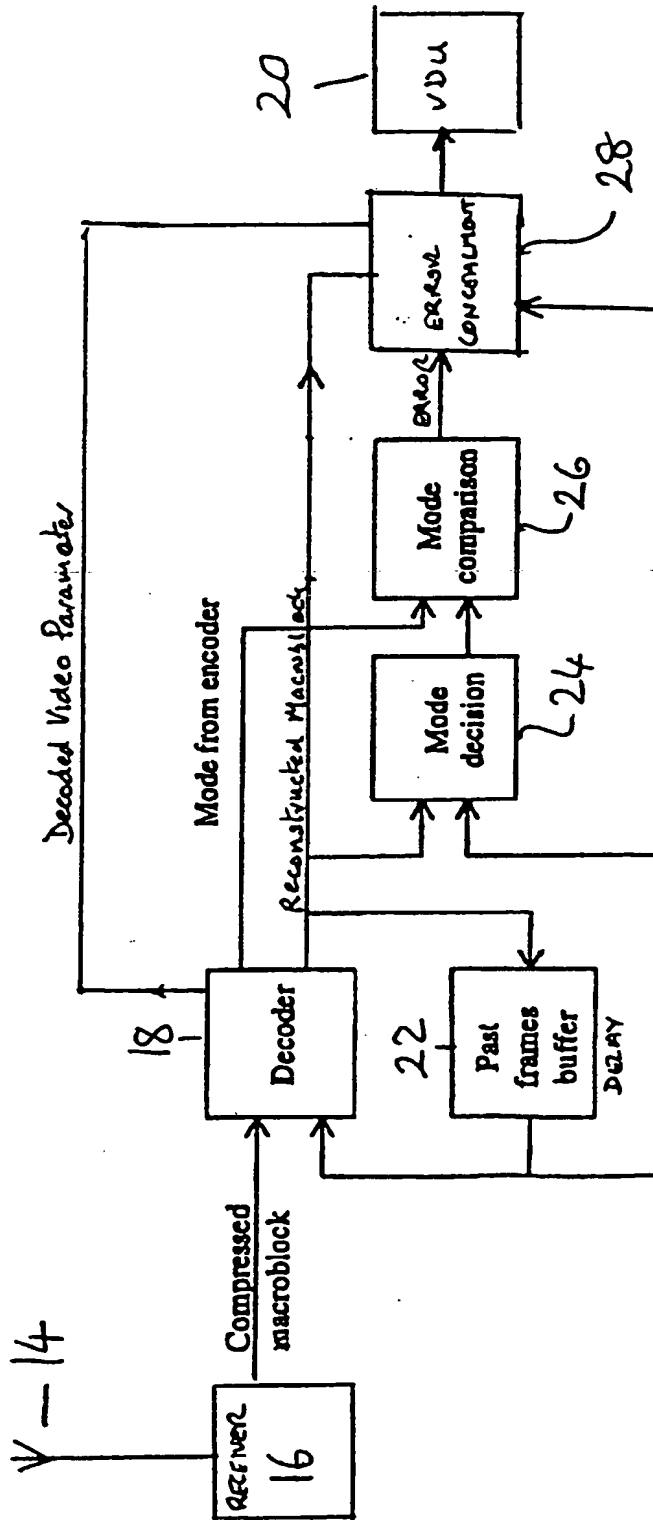


FIG. 2



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EUROPEAN SEARCH REPORT

Application Number
EP 99 30 6911

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The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 11 January 2000	Examiner Foglia, P
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